## 2-2 TELLEGEN'S THEOREM

Next, an important law of circuit theory, Tellegen's theorem, will be introduced. This basic theorem will help us understand the fundamental properties of physically realizable impedance functions discussed later in this chapter. It will also be used much later in the book (in Chaps. 9 and 10) in connection with the sensitivity analysis of circuits.

Consider the circuit shown in Fig. 2-4a. With the notations of Fig. 2-4b, the Kirchhoff current laws (KCL) give

$$
\begin{align*}
j_{1}+j_{2}+j_{5} & =0 \\
-j_{2}+j_{3}+j_{4} & =0  \tag{2-27}\\
-j_{4}-j_{5}+j_{6} & =0
\end{align*}
$$


(a)

(b)

Figure 2-4 (a) Linear circuit; $(b)$ notations used in the analysis

Note the use of associated directions ${ }^{1}$ for $v_{k}$ and $j_{k}$. Equations (2-27) can also be written in the form $\dagger$

$$
\begin{equation*}
\mathbf{A} \mathbf{j}=\mathbf{0} \tag{2-28}
\end{equation*}
$$

where $\mathbf{A}$ is the incidence matrix

$$
\mathbf{A}=\left[\begin{array}{rrrrrr}
1 & 1 & 0 & 0 & 1 & 0  \tag{2-29}\\
0 & -1 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & -1 & -1 & 1
\end{array}\right] \begin{aligned}
& 1 \\
& 2
\end{aligned}
$$

The elements of $\mathbf{A}$ are given by

$$
a_{i j}=\left\{\begin{align*}
+1 & \text { if branch } j \text { leaves node } i  \tag{2-30}\\
-1 & \text { if branch } j \text { enters node } i \\
0 & \text { if branch } j \text { is not incident with node } i
\end{align*}\right.
$$

Similarly, from the Kirchhoff voltage law (KVL)

$$
\begin{array}{lr}
v_{1}=e_{1} \\
v_{2} & =e_{1}-e_{2} \\
v_{3} & =e_{2} \\
v_{4} & =\quad e_{2}-e_{3}  \tag{2-31}\\
v_{5} & =e_{1} \\
v_{6} & =\quad-e_{3} \\
\end{array}
$$

or, using (2-29),

$$
\begin{equation*}
\mathbf{v}=\mathbf{A}^{T} \mathbf{e} \tag{2-32}
\end{equation*}
$$

where $\mathbf{A}^{T}$ denotes the transpose of $\mathbf{A}$. Equations (2-28) and (2-32) are, of course, the general matrix forms of the KCL and KVL, respectively. ${ }^{1}$

Since all relations (2-27) to (2-32) involve only additions and subtractions, they remain valid if we perform any linear operations on the $j_{i}, v_{k}$, and $e_{l}$. For example, they hold also for the Laplace transforms $J_{i}(s), V_{k}(s)$, and $E_{l}(s)$ or for the phasors $J_{i}, V_{k}$, and $E_{l}$, etc.

Let the branch power $v_{k} j_{k}$ be summed for all $N$ branches of the circuit. Then, by (2-32) and (2-28),

$$
\begin{equation*}
\sum_{k=1}^{N} v_{k} j_{k}=\mathbf{v}^{T} \mathbf{j}=\left(\mathbf{A}^{T} \mathbf{e}\right)^{T} \mathbf{j}=\mathbf{e}^{T}(\mathbf{A} \mathbf{j})=0 \tag{2-33}
\end{equation*}
$$

Here the familiar rules $(\mathbf{A e})^{T}=\mathbf{e}^{T} \mathbf{A}^{T},\left(\mathbf{A}^{T}\right)^{T}=\mathbf{A}$, and $\mathbf{e}^{T} \mathbf{0}=0$ of vector algebra have been used.

Equation (2-33) is equivalent, of course, to the conservation of power in the circuit, and thus its emergence from Kirchhoff's laws is not too surprising.

[^0]

Figure 2-5 Circuit with the same configuration as that of Fig. 2-4 but with different elements.

Consider, however, the circuit of Fig. 2-5, which has the same topological configuration, same reference directions and numbering, and hence the same $\mathbf{A}$ as the circuit of Fig. 2-4. Hence, all the equations (2-27) to (2-32) remain valid for this circuit as well, with A remaining the same. Let the electric quantities of the circuit of Fig. 2-5 be $\mathbf{j}^{\prime}, \mathbf{v}^{\prime}$, and $\mathbf{e}^{\prime}$. Then
and

$$
\mathbf{A j}^{\prime}=\mathbf{0}
$$

hold.
Next, let the physically meaningless quantity $\sum_{k=1}^{N} v_{k} j_{k}^{\prime}$ be found:

$$
\begin{equation*}
\sum_{k=1}^{N} v_{k} j_{k}^{\prime}=\mathbf{v}^{T} \mathbf{j}^{\prime}=\left(\mathbf{A}^{T} \mathbf{e}\right)^{T} \mathbf{j}^{\prime}=\mathbf{e}^{T}\left(\mathbf{A} \mathbf{j}^{\prime}\right)=0 \tag{2-36}
\end{equation*}
$$

where (2-32) and (2-34) were used. While the left-hand side of (2-36) does have the dimension of watts, it does not correspond to physical power since $v_{k}$ and $j_{k}^{\prime}$ exist in two different circuits.

An entirely analogous derivation gives

$$
\begin{equation*}
\sum_{k=1}^{N} v_{k}^{\prime} j_{k}=\mathbf{v}^{\prime} \mathbf{T} \mathbf{j}=0 \tag{2-37}
\end{equation*}
$$

Equations (2-36) and (2-37) are general forms and (2-33) is a special form of Tellegen's theorem. The general forms have great significance (as will be shown in Chaps. 9 and 10) in the calculation of circuit sensitivities.

Consider now a linear passive RLCM one-port (Fig. 2-6). By (2-32), using Laplace transformation,

$$
\begin{equation*}
\mathbf{V}(s)=\mathbf{A}^{T} \mathbf{E}(s) \tag{2-38}
\end{equation*}
$$

Physical proof:

Construct a new network NN with the same topology. Choose a tree. Put voltage sources in the tree branches, whose value equal the corresponding branch voltages of $\mathrm{N}^{\prime}$, and put current sources in the links whose values equal the corresponding link of N .

Thus, conservation of power gives Tellegen's theorem.


Figure 2-6 $R L C M$ one-port.
and from (2-28), using Laplace transformation and taking the complex conjugate,

Hence

$$
\begin{gather*}
\mathbf{A} \mathbf{J}^{*}(s)=\mathbf{0}  \tag{2-39}\\
\mathbf{V}^{T} \mathbf{J}^{*}=\mathbf{E}^{T} \mathbf{A} \mathbf{J}^{*}=0 \tag{2-40}
\end{gather*}
$$

or

$$
\begin{equation*}
\sum_{k=1}^{N} V_{k}(s) J_{k}^{*}(s)=0 \tag{2-41}
\end{equation*}
$$

From Fig. 2-6 and Eq. (2-41), using $i_{1}=-j_{1}$, we get

$$
\begin{equation*}
-V_{1}(s) J_{1}^{*}(s)=V_{1}(s) I_{1}^{*}(s)=\sum_{k=2}^{N} V_{k} J_{k}^{*} \tag{2-42}
\end{equation*}
$$

Note that branches 2 to $N$ are inside the one-port.
Defining the impedance of the one-port as the ratio of $V_{1}(s)$ and $I_{1}(s)$,

$$
Z(s) \triangleq \frac{V_{1}(s)}{I_{1}(s)}=\frac{V_{1}(s) I_{1}^{*}(s)}{I_{1}(s) I_{1}^{*}(s)}=\frac{V_{1}(s) I_{1}^{*}(s)}{\left|I_{1}(s)\right|^{2}}
$$

leads to

$$
\begin{equation*}
Z(s)=\frac{1}{\left|I_{1}(s)\right|^{2}} \sum_{k=2}^{N} V_{k}(s) J_{k}^{*}(s) \tag{2-43}
\end{equation*}
$$

It is easy to show, using an entirely analogous derivation, that the dual relation

$$
\begin{equation*}
Y(s) \triangleq \frac{I_{1}(s)}{V_{1}(s)}=\frac{1}{\left|V_{1}(s)\right|^{2}} \sum_{\substack{\text { all internal } \\ \text { branches }}} V_{k}^{*}(s) J_{k}(s) \tag{2-44}
\end{equation*}
$$

holds for the input admittance $Y(s)$ of the one-port.
Equations (2-43) and (2-44) are fundamental to the analysis and design of one-ports, as will be shown in the next section.

Network Functions

## Driving-Point and Transfer Functions for Linear Networks

Node equations: $\underline{Y_{n}}(s) \underline{E}(s)=\underline{J_{n}}(s)$ can (in principle) be solved using Cramer's Rule, so that the node voltages are given by

$$
\begin{gather*}
\underline{E}(s)=\underline{Y n}_{n}^{-1}(s) \underline{J_{n}}(s)  \tag{a}\\
\underline{Y_{n} E}=\underline{J_{n}}=\underline{A}\left(\underline{J}-\underline{Y V_{e}}\right) \tag{b}
\end{gather*}
$$

Where the ij element of the inverse matrix is $\frac{\Delta_{i j}}{\Delta}$, $\Delta$ being the determinant of $\mathrm{Y}_{\mathrm{n}}$ and $\Delta_{\mathrm{ij}}$ its ij cofactor (signed subdeterminant). For a lumped linear circuit, $\Delta$ and the $\Delta_{\mathrm{ij}}$ are all real and rational is $s$.

It follows from equation (a) that all response voltages and currents are weighted sums of the excitations, which enter $J_{n}(s)$.

example:
Lumped Linear Network

Fig. 1. Illustration for equivalent sources.

$$
\begin{gathered}
\mathbf{J}=\left[\begin{array}{l}
J_{1} \\
J_{2} \\
J_{3}
\end{array}\right]=\left[\begin{array}{c}
I_{g 2}+Y_{1} V_{g 1} \\
-I_{g 2}+I_{g 3} \\
-Y_{1} V_{g 1}
\end{array}\right] .
\end{gathered} \begin{aligned}
& \text { Nodal current } \\
& V_{k}(s)=\left(\frac{Y_{1} \Delta_{1 k}-Y_{1} \Delta_{3 k}}{\Delta}\right) V_{g 1}+\left(\frac{\Delta_{1 k}-\Delta_{2 k}}{\Delta}\right) I_{g 2}+\left(\frac{\Delta_{2 k}}{\Delta}\right) I_{g 3} . \quad \mathrm{k}=1,2,3 \\
& \underline{E}(s)=\left[\begin{array}{lll}
\Delta_{11} & \Delta_{12} & \Delta_{13} \\
\Delta_{21} & \Delta_{22} & \Delta_{23} \\
\Delta_{31} & \Delta_{32} & \Delta_{33}
\end{array}\right] \underline{\underline{J}(s)} \Delta
\end{aligned}
$$

## Driving-Point Functions (Immittances)

One-port circuit:


## RLCM

Driving-point impedance:

$$
Z(s)=\frac{V_{1}(s)}{I_{1}(s)}
$$

Driving-point admittance:

$$
Y(s)=\frac{I_{1}(s)}{V_{1}(s)}=\frac{1}{Z(s)}
$$

From (a), $Z(s)=\frac{\Delta_{11}(s)}{\Delta(s)}$; a real rational function of s .

The positive real (PR) property:
$\mathrm{Z}(\mathrm{s})$ is a PR function of s if $\mathfrak{R e} Z(s) \geq 0$ for $\mathfrak{R e} s \geq 0$

## Brune's Theorem:

Any real rational PR Z(s) can be realized using physical RLCM elements (R, L, C all $\geq 0$ ), and vice versa, any such physical impedance must satisfy the real rational PR conditions.

## Proof of the PR property:

Consider a circuit containing only $\mathrm{R}, \mathrm{L}$, and C elements. For an $\mathrm{R}, V=R \cdot J$, so $V \cdot J^{*}=R \cdot|J|^{2}$. Similarly, for an $\mathrm{L}, V \cdot J^{*}=s L \cdot|J|^{2}$, and for a $\mathrm{C}, V \cdot J^{*}=\frac{1}{s C} \cdot|J|^{2}$. Hence, for the complete network, substituting into Tellegen's Theorem with $\mathrm{J}_{1}=1 \mathrm{~A}$.

$$
Z(s)=\sum_{k} V_{k}(s) J_{k}^{*}(s)=F_{o}(s)+\frac{1}{s} V_{o}(s)+s T_{o}(s)
$$

Where,

$$
F_{o}(s)=\sum_{k} R_{k}\left|J_{k}\right|^{2}, \quad V_{o}(s)=\sum_{k} \frac{\left|J_{k}\right|^{2}}{C_{k}}, \text { and } T_{o}(s)=\sum_{k} L_{k}\left|J_{k}\right|^{2}
$$

For a physical circuit, all $\mathrm{R}_{\mathrm{k}}, \mathrm{L}_{\mathrm{k}}, \mathrm{C}_{\mathrm{k}}$ are non-negative real numbers, and hence so are $\mathrm{F}_{\mathrm{o}}$, $V_{0}$, and $T_{0}$, for any $s$.

Next let

$$
\begin{gathered}
s=\sigma+j \omega \\
\frac{1}{\sigma+j \omega}=\frac{\sigma-j \omega}{\sigma^{2}+\omega^{2}} \Rightarrow \frac{\sigma}{\sigma^{2}+\omega^{2}} \geq 0
\end{gathered}
$$

Where $\sigma \geq 0$. Then both $\mathfrak{R e}(s)$ and $\mathfrak{R e}\left(\frac{1}{s}\right)=\frac{\sigma}{\sigma^{2}+\omega^{2}}$ are non-negative, and hence so is $\mathfrak{R e Z}(s)$.

The above can be extended to transformers.

The proof of the sufficiency of the real rational PR conditions is based on a synthesis, which always leads to physical element values. It uses resistors, capacitors and closely coupled transformers.

Brune synthesis

$$
\frac{s^{2}+\ldots s+3}{s^{5}+\ldots 2 s+1}
$$



Figure 4-12 (a) Brune realization of an impedance $Z(s)$; $(b)$ realizations for the last impedance $Z_{i}$.

Node Analysis Summary

| $\underline{A}$ | Incidence matrix, all analysis in s domain |
| :--- | :--- |


| Kirchhoff's Laws: |  |
| :--- | :--- |
| $\underline{V}=\underline{A}^{t} \underline{E}$ | $\underline{V}:$ branch voltage vector |
| $\underline{E}:$ node voltage vector |  |$]$| $\underline{I}:$ branch current vector |
| :--- |
| $\underline{0}:$ zero vector |


| Branch Relations: |  |
| :--- | :--- |
| $\underline{I}=\underline{I}-\underline{J}$ | $\underline{I}:$ branch current vector |
|  | $\underline{I}:$ element current vector |
|  | $\underline{J}:$ souce current vector |
| $\underline{V}^{\prime}=\underline{V}-\underline{V_{E}}$ | $\underline{V^{\prime}}:$ branch voltage vector |
|  | V :element voltage vector <br>  <br> $\underline{V_{E}}:$ souce voltage vector |
| $\underline{I}=\underline{Y} \underline{V}$ | $\underline{Y}:$ branch admittance matrix |


| Combining relations: |  |
| :--- | :--- |
| $\underline{Y_{N}}=\underline{A} \underline{Y} \underline{A}^{t}$ | $\underline{Y_{N}}:$ node admit $\tan$ ce matrix |
| $\underline{J_{N}}=\underline{A}[\underline{J}-\underline{Y} \underline{V}]$ | $\underline{J_{N}}:$ node current excitation vector |
| $\underline{Y_{N}} \underline{E}=\underline{J_{N}}$ | generaizednode node equations |


[^0]:    $\dagger$ Here, and in the rest of the book, $\mathbf{x}$ denotes a column vector and $\mathbf{A}$ denotes a matrix.

